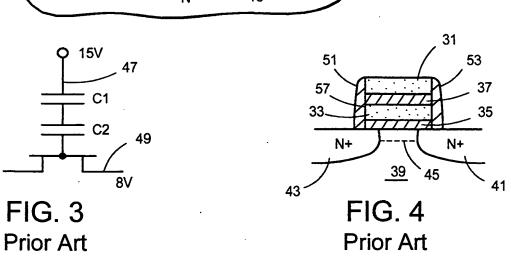
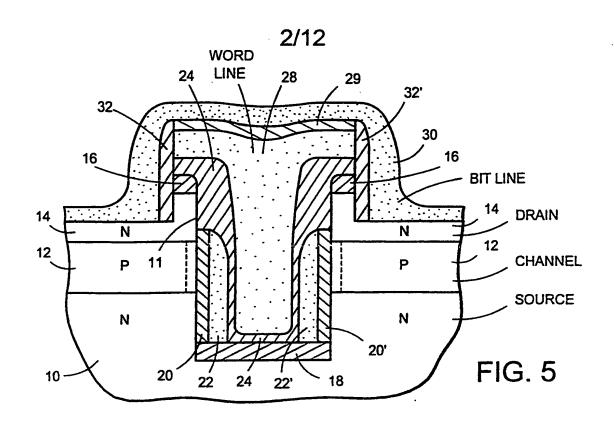
Application No.: 08/654,760 Inventor: VORA, et al. Docket No.: V&F-001 VERTICALLY INTEGRATED FLASH EPROM FOR GREATER DENSITY AND LOWER COST 1/12 16 16 14 14 Ν 22 Ν 12 12 20 P Ρ 20' N Ν 11 FIG. 1 10 18 Ν 28 24 16 16 14 14 Ν N 12 12 20 P Ρ N Ν 11 FIG. 2 10 18 N 31 O 15V 53 51 47 37 57 \_ C1 33~ 35 C2





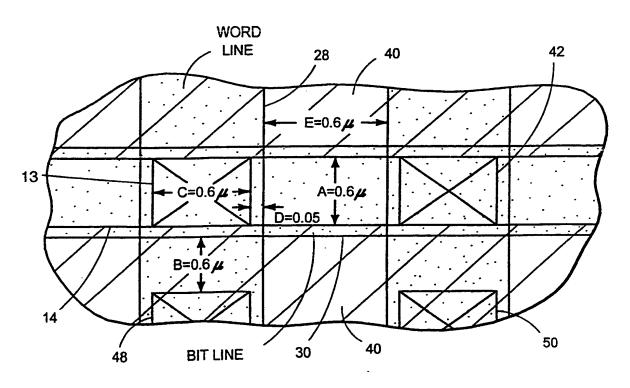
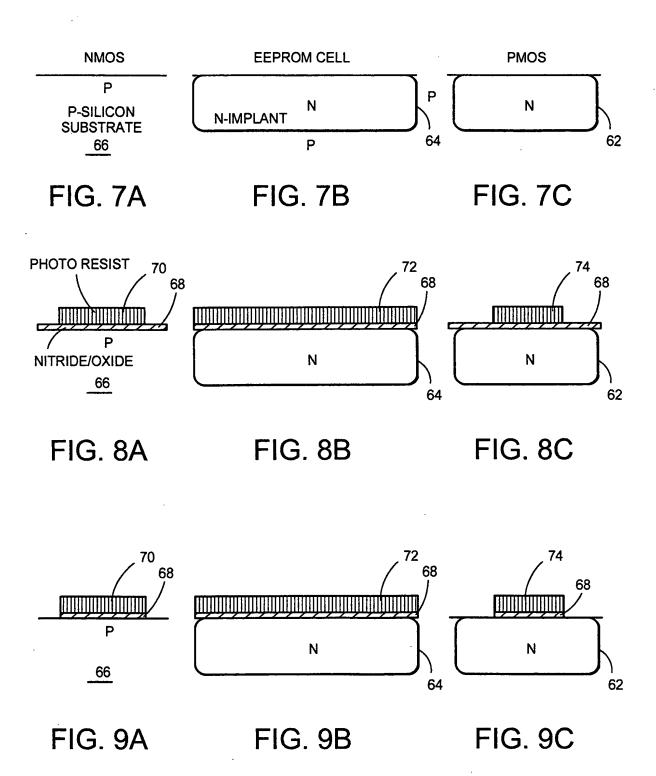
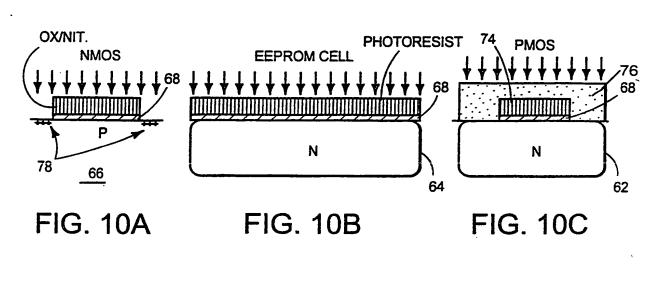
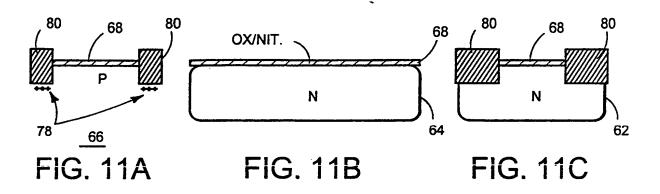


FIG. 6







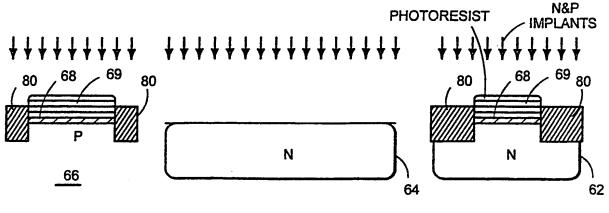
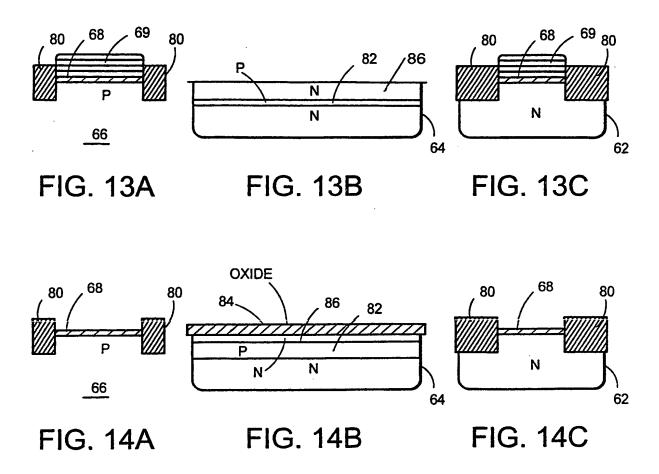
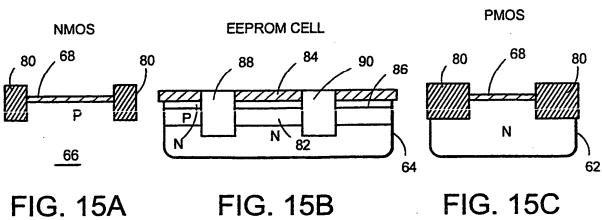


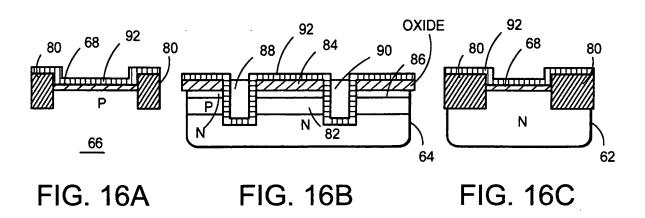
FIG. 12A

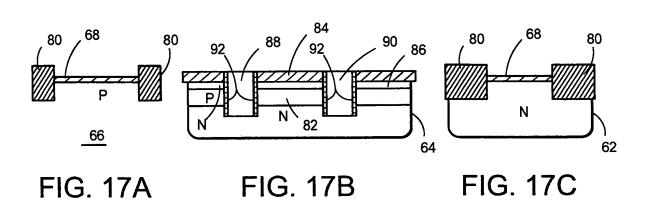
FIG. 12B

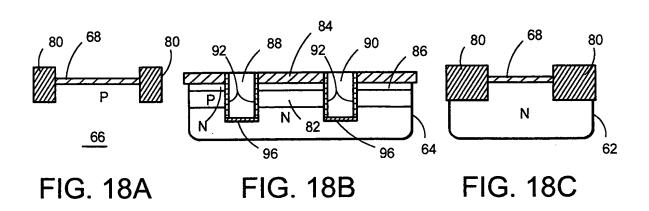
FIG. 12C

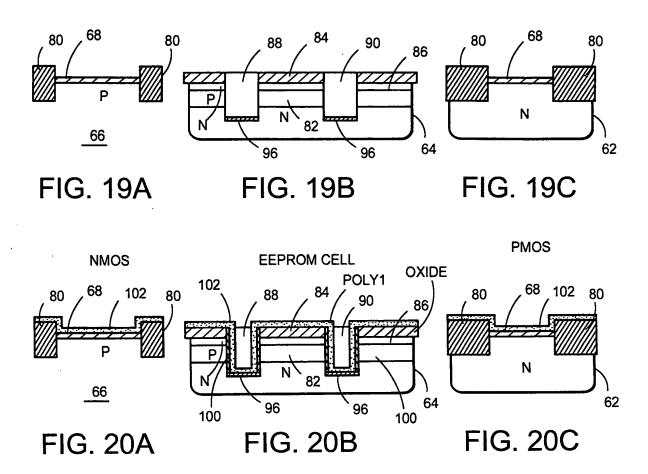


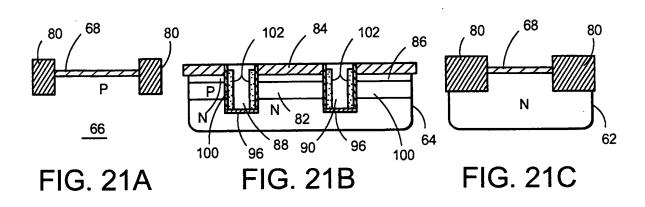


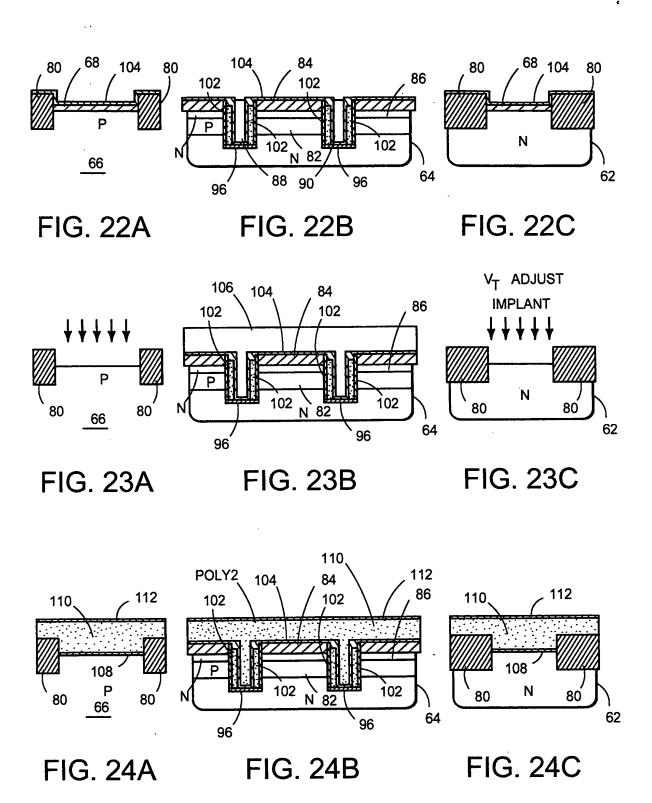


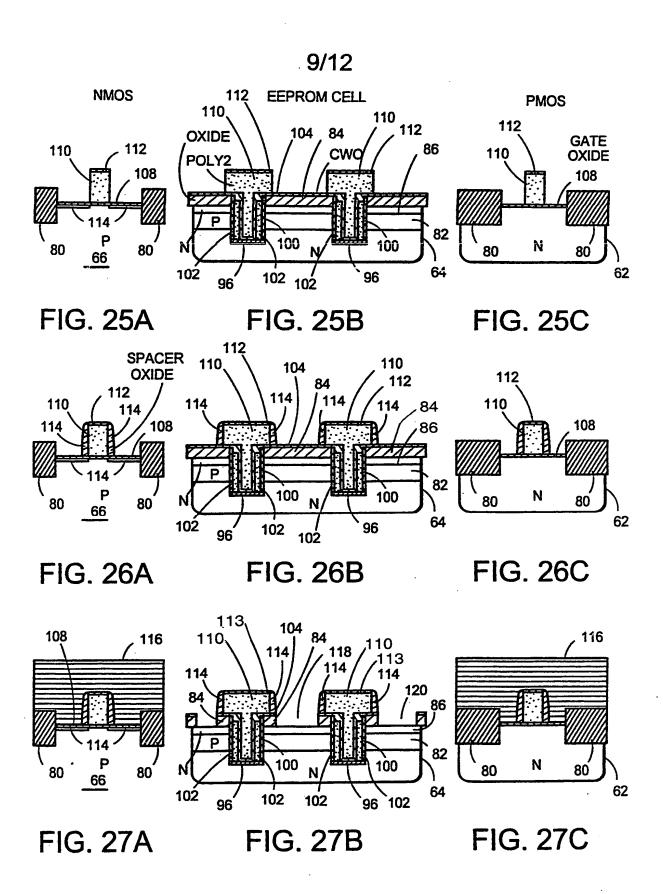




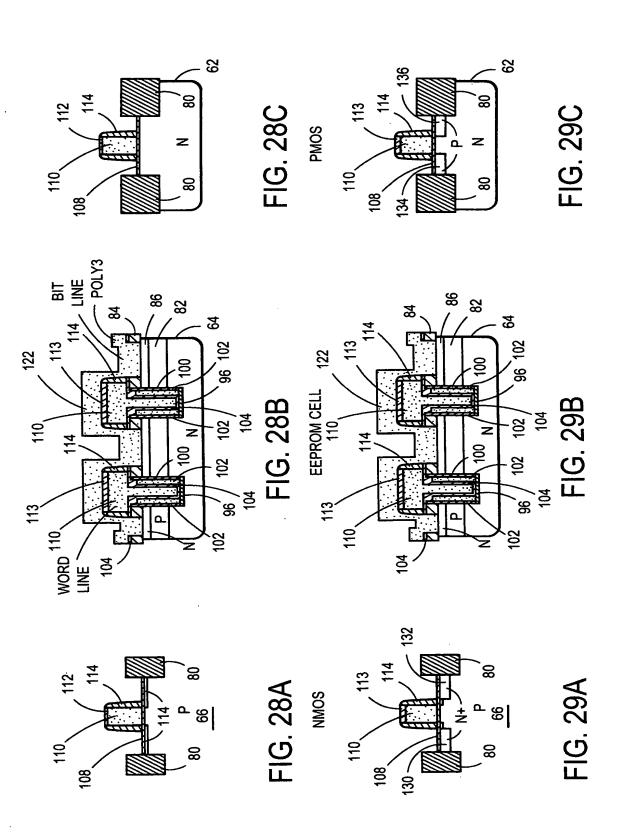




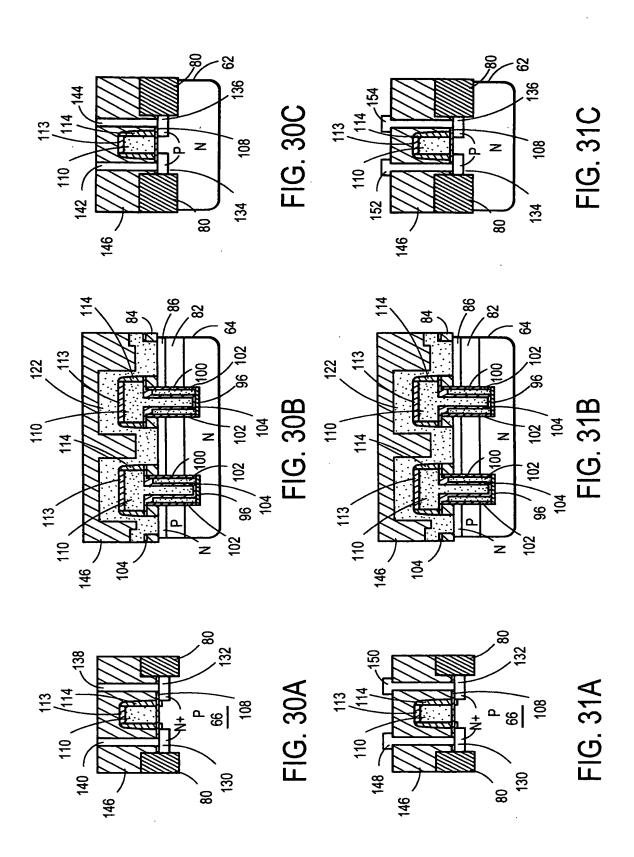




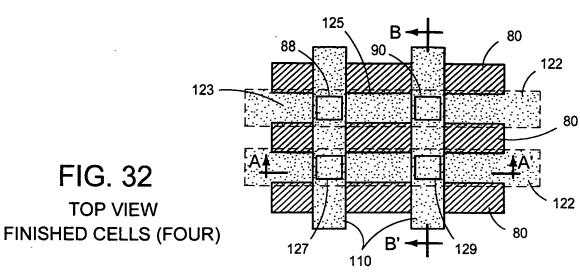
10/12



11/12



12/12



110, FIG. 33 **SECTION A-A'** 96 102 104. 

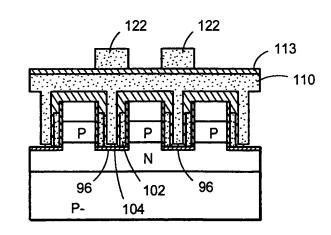


FIG. 34 **SECTION B-B'**